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394-1599UBLISHING DIVISION

96 FEB 21 PM 3: 00 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In se Application of

Seiji SAWADA et al.

Serial No. 08/246,582

Filed: May 19, 1994

Group Art Unit: A

Examiner: A. Zarabian

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE

<u>Letter</u>

RECEIVED
JAN 2 5 1996
GROUP 2500

Honorable Commissioner of Patents and Trademarks Washington, DC 20231

Sir:

On November 30, 1995, an amendment was inadvertingly filed in the above-referenced application. Please disregard the amendment as it should have referenced the divisional application Serial Number 08/461,907, filed June 5, 1995. The amendment has be submitted in the correct application on January 2, 1996.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

Edward J. Wise

Registration No. 34,523

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Date: January 2, 1996